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Washington, D.C. 20231

May 14, 2001  
Date of Deposit

John G. Rauch, Reg. No. 37,218  
Name of applicant, assignee or  
Registered Representative

John G. Rauch  
Signature

5/14/2001  
Date of Signature

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MAY 17 2001  
Technology Center 2100  
Our Case No. 10521-4

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
J. Howard Smith et al. )  
Serial No. 09/780,979 ) Examiner: To Be Assigned  
Filing Date: February 9, 2001 ) Group Art Unit: 2185  
For DATA COMMUNICATION CONTROLLER )  
AND METHOD )

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in  
conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit the  
references listed below and on the attached form PTO-1449 for



consideration by the Examiner. A copy of each of the references is  
enclosed herewith.

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MAY 17 2001  
Technology Center 2100

### U. S. PATENT DOCUMENTS

5,001,642	3/19/91	Botzenhardt et al.
5,105,441	4/14/92	Borst et al.
5,111,460	5/5/92	Botzenhardt et al.
5,216,674	6/1/93	Peter et al.
5,303,348	4/12/94	Botzenhardt et al.
5,357,518	10/18/94	Peter
5,448,180	9/5/95	Kienzler
5,448,561	9/5/95	Kaiser et al.
5,499,336	3/12/96	Preis et al.
5,524,213	6/4/96	Dais et al.
5,539,778	7/23/96	Kienzler et al.
5,621,888	4/15/97	Botzenhardt et al.
5,640,511	6/17/97	Botzenhardt et al.
5,901,156	5/4/99	Botzenhardt et al.

### FOREIGN PATENT DOCUMENT

DOCUMENT NUMBER	DATE	COUNTRY
DE 39 27 968 A1	2/28/91	Germany

### OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Bosch, Robert, "CAN Specification", Version 2.0, Robert Bosch Gmbh, Germany, 1991, 23 pages
Intel, "82527 Serial Communications Controller - Controller Area Network Protocol - Automotive", Intel Corporation publication, December 1995, 22 pages
SYNERGETIC, "Revolutionizing Embedded Communications", SYNERGETIC publication, 2000, 7 pages
Lue, Daniel, "CANmodule for FactoryCOMM-1", INICORE, Inc. publication, Version 3.2, May 2000, 20 pages
SYNERGETIC, "FactoryComm FC-1 Data Sheet, Confidential and Advanced Information, SYNERGETIC FacytoryComm Tools publication, July 2000, 12 pages
Copy of claims for co-pending application U.S. Serial No. 09/780,985, filed on February 9, 2001, 4 pages

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest these references as prior art against the present application, and Applicants do not believe that the disclosure of this reference, even if finally determined to be prior art, anticipates Applicant's invention or that these references make Applicant's invention obvious.

With respect to the German patent publication number DE 39 27 968 A1, Applicants submit the following translation of the Abstract of this patent publication. This Abstract is taken from the Derwent patent database.

The method of transferring data in a distributed signal processing system with signal processing devices intercommunicating via a serial data bus uses messages consisting of an identifier (I), an instruction (X) and a conditional action (Y) for performing the instruction. At least one message component is transferred via the data bus to the performing processor at a defined time point, esp. before occurrence of the action. The identifier is associated with an algorithm which checks the occurrence of the action and triggers performance of the instruction.

Further, Applicants provide an English translation of the Abstract of the European patent number EP 439559 B:

Method for transmitting data in data processing systems with distributed signal processing devices, in particular control devices for motor vehicles, such as ignition devices, injection devices, brake devices and/or transmission control devices,

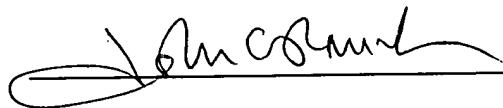
which communicate with one another via a serial data bus, messages being transmitted between the said devices and one message having at least one identifier to which an instruction is assigned, characterised in that the message is additionally assigned a determining event (Y) for the execution of the instruction (X), that at least the identifier (I) is transmitted at any desired time before the occurrence of the determining event (Y) for the execution of the instruction (X) via the data bus to the function-executing signal processing device, the identifier (I) in the function-executing signal processing device being assigned a control algorithm (A) which tests for the occurrence of the event (Y) and triggers the execution of the instruction when the event occurs.

Applicants also submit for the Examiner's consideration co-pending U.S. application Serial No. 09/780,985, filed on February 9, 2001, and commonly assigned to the assignee of the present application, Synergetic Micro Systems, Inc.

This Information Disclosure Statement is being filed prior to the receipt of the first Official Action reflecting an examination on the merits and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with filing of this Information Disclosure Statement. However, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925.

Applicants respectfully request that the Examiner review the entire disclosure of these documents and make them of record.

Respectfully submitted,



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